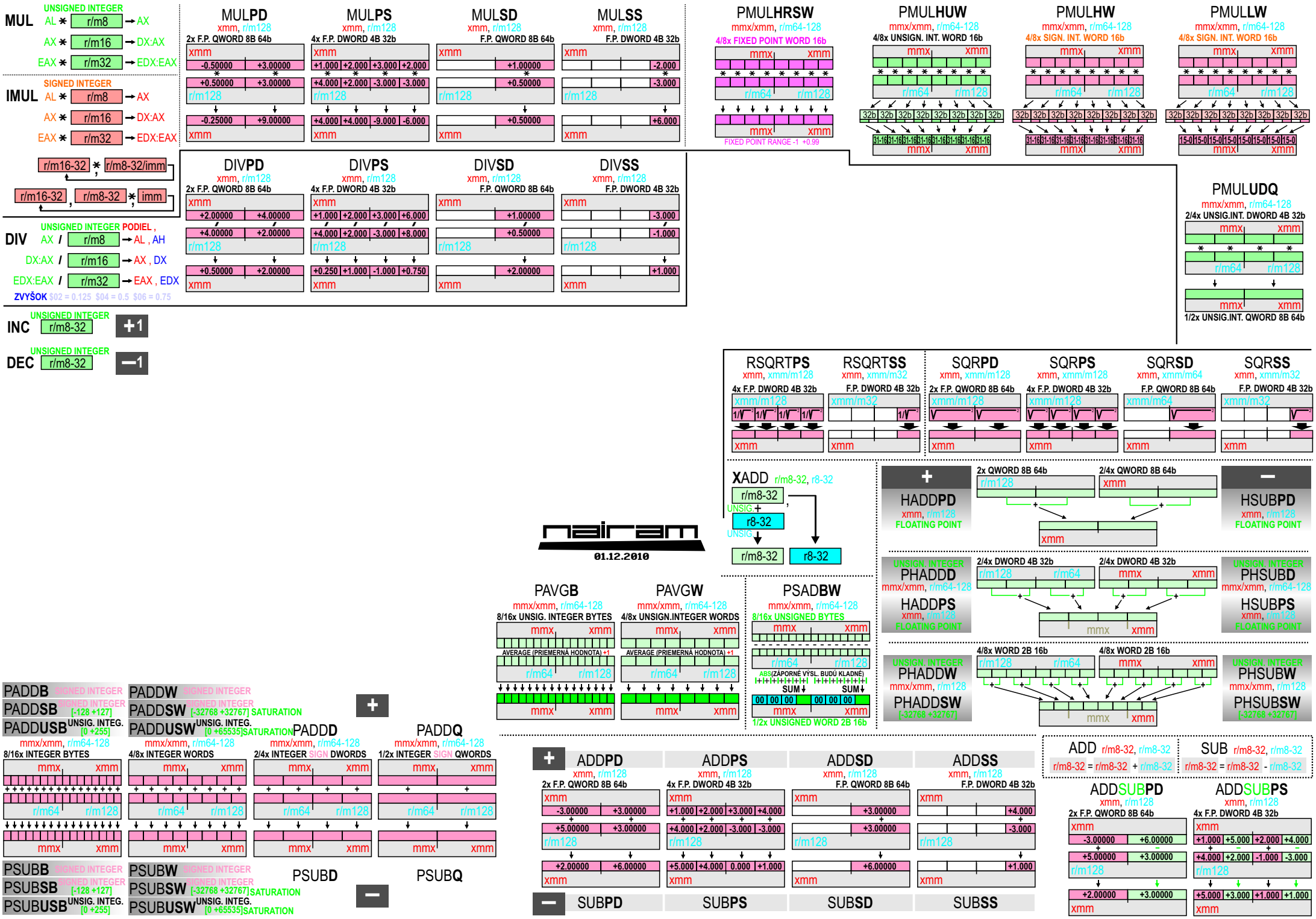
MUL
UNSIGNED INTEGER
AL * r/m8 → AX
AX * r/m16 → DX:AX
EAX * r/m32 → EDX:EAX
IMUL
SIGNED INTEGER
AL * r/m8 → AX
AX * r/m16 → DX:AX
EAX * r/m32 → EDX:EAX
r/m16-32 * r/m8-32/imm
r/m16-32 , r/m8-32 * imm
DIV
UNSIGNED INTEGER PODIEL ,
AX / r/m8 → AL , AH
DX:AX / r/m16 → AX , DX
EDX:EAX / r/m32 → EAX , EDX
ZVYŠOK $02 = 0.125 $04 = 0.5 $06 = 0.75
INC
UNSIGNED INTEGER
r/m8-32
+1
DEC
UNSIGNED INTEGER
r/m8-32
−1
MULPD
xmm, r/m128
2x F.P. QWORD 8B 64b
MULPS
xmm, r/m128
4x F.P. DWORD 4B 32b
MULSD
xmm, r/m128
F.P. QWORD 8B 64b
MULSS
xmm, r/m128
F.P. DWORD 4B 32b
DIVPD
xmm, r/m128
2x F.P. QWORD 8B 64b
DIVPS
xmm, r/m128
4x F.P. DWORD 4B 32b
DIVSD
xmm, r/m128
F.P. QWORD 8B 64b
DIVSS
xmm, r/m128
F.P. DWORD 4B 32b
PMULHRSW
mmx/xmm, r/m64-128
4/8x FIXED POINT WORD 16b
FIXED POINT RANGE -1 +0.99
PMULHUW
mmx/xmm, r/m64-128
4/8x UNSIGN. INT. WORD 16b
PMULHW
mmx/xmm, r/m64-128
4/8x SIGN. INT. WORD 16b
PMULLW
mmx/xmm, r/m64-128
4/8x SIGN. INT. WORD 16b
PMULUDQ
mmx/xmm, r/m64-128
2/4x UNSIG.INT. DWORD 4B 32b
1/2x UNSIG.INT. QWORD 8B 64b
RSQRTPS
xmm, xmm/m128
4x F.P. DWORD 4B 32b
RSQRTSS
xmm, xmm/m32
F.P. DWORD 4B 32b
SQRPD
xmm, xmm/m128
2x F.P. QWORD 8B 64b
SQRPS
xmm, xmm/m128
4x F.P. DWORD 4B 32b
SQRSD
xmm, xmm/m64
F.P. QWORD 8B 64b
SQRSS
xmm, xmm/m32
F.P. DWORD 4B 32b
XADD r/m8-32, r8-32
r/m8-32
UNSIG.+
r8-32
UNSIG.
r/m8-32
r8-32
nairam
01.12.2010
PAVGB
mmx/xmm, r/m64-128
8/16x UNSIG. INTEGER BYTES
AVERAGE (PRIEMERNÁ HODNOTA) +1
PAVGW
mmx/xmm, r/m64-128
4/8x UNSIGN.INTEGER WORDS
AVERAGE (PRIEMERNÁ HODNOTA) +1
PSADBW
mmx/xmm, r/m64-128
8/16x UNSIGNED BYTES
ABS(ZÁPORNÉ VÝSL. BUDÚ KLADNÉ)
SUM
SUM
1/2x UNSIGNED WORD 2B 16b
+
HADDPD
xmm, r/m128
FLOATING POINT
2x QWORD 8B 64b
2/4x QWORD 8B 64b
−
HSUBPD
xmm, r/m128
FLOATING POINT
UNSIGN. INTEGER
PHADDD
mmx/xmm, r/m64-128
HADDPS
xmm, r/m128
FLOATING POINT
2/4x DWORD 4B 32b
2/4x DWORD 4B 32b
UNSIGN. INTEGER
PHSUBD
mmx/xmm, r/m64-128
HSUBPS
xmm, r/m128
FLOATING POINT
UNSIGN. INTEGER
PHADDW
mmx/xmm, r/m128
PHADDSW
[-32768 +32767]
4/8x WORD 2B 16b
4/8x WORD 2B 16b
UNSIGN. INTEGER
PHSUBW
mmx/xmm, r/m128
PHSUBSW
[-32768 +32767]
PADDB SIGNED INTEGER
PADDSB SIGNED INTEGER [-128 +127]
PADDUSB UNSIG. INTEG. [0 +255]
PADDW SIGNED INTEGER
PADDSW SIGNED INTEGER [-32768 +32767] SATURATION
PADDUSW UNSIG. INTEG. [0 +65535] SATURATION
+
PADDD
mmx/xmm, r/m64-128
2/4x INTEGER SIGN DWORDS
PADDQ
mmx/xmm, r/m64-128
1/2x INTEGER SIGN QWORDS
mmx/xmm, r/m64-128
8/16x INTEGER BYTES
4/8x INTEGER WORDS
PSUBB SIGNED INTEGER
PSUBSB SIGNED INTEGER [-128 +127]
PSUBUSB UNSIG. INTEG. [0 +255]
PSUBW SIGNED INTEGER
PSUBSW SIGNED INTEGER [-32768 +32767] SATURATION
PSUBUSW UNSIG. INTEG. [0 +65535] SATURATION
PSUBD
PSUBQ
−
+
ADDPD
xmm, r/m128
2x F.P. QWORD 8B 64b
ADDPS
xmm, r/m128
4x F.P. DWORD 4B 32b
ADDSD
xmm, r/m128
F.P. QWORD 8B 64b
ADDSS
xmm, r/m128
F.P. DWORD 4B 32b
−
SUBPD
SUBPS
SUBSD
SUBSS
ADD r/m8-32, r/m8-32
r/m8-32 = r/m8-32 + r/m8-32
SUB r/m8-32, r/m8-32
r/m8-32 = r/m8-32 - r/m8-32
ADDSUBPD
xmm, r/m128
2x F.P. QWORD 8B 64b
ADDSUBPS
xmm, r/m128
4x F.P. DWORD 4B 32b